

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Number	:	10/595,303	Confirmation No.	:	7114
Applicant	:	Roy Knechtel			
Filed	:	May 19, 2006			
Title	:	METHOD AND DEVICE FOR SECURE, INSULATED AND ELECTRICALLY CONDUCTIVE ASSEMBLING OF TREATED SEMICONDUCTOR WAFERS			
TC/Art Unit	:	2823			
Examiner:		Kevin A. Parendo			
Docket No.	:	60291.000048			
Customer No.	:	21967			

MAIL STOP AMENDMENT  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

AMENDMENT/RESPONSE

Sir:

Responsive to the Non-Final Office Action ("Office Action") dated December 19, 2008, Applicant respectfully requests favorable reconsideration of the above identified patent application in view of the following amendments and remarks.

Amendments to the claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 7.